DECEMBER 8, 2000

TITLE: A METHOD AND APPARATUS FOR DIGITAL CHANNELISATION

AND DE-CHANNELISATION INVENTOR(S): RIC

RICHARD HELLBERG

ATTORNEY DOCKET No.:

003250-235

SHEET 1 OF 10

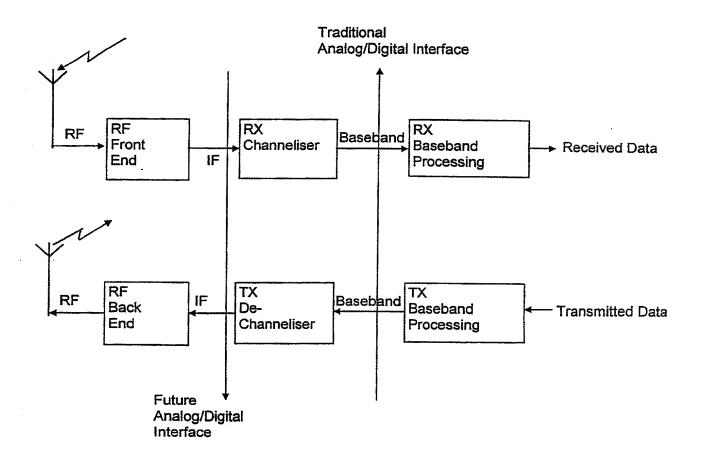


Fig. 1

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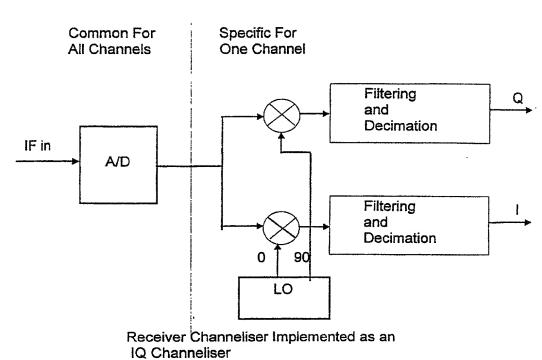


Fig 2

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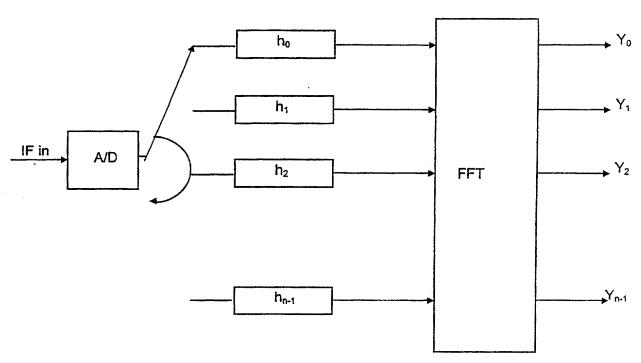
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Receiver Channeliser Implemented Via A Decimated Filter Bank

Fig. 3

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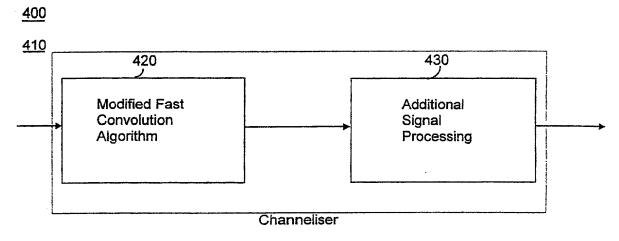
AND DE-CHANNELISATION

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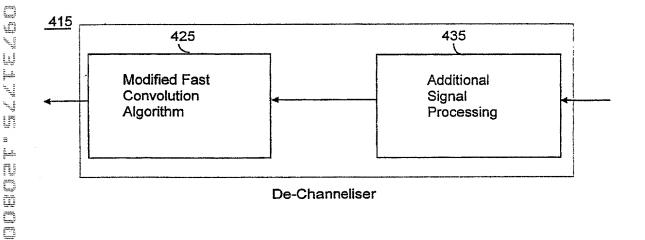


Fig. 4

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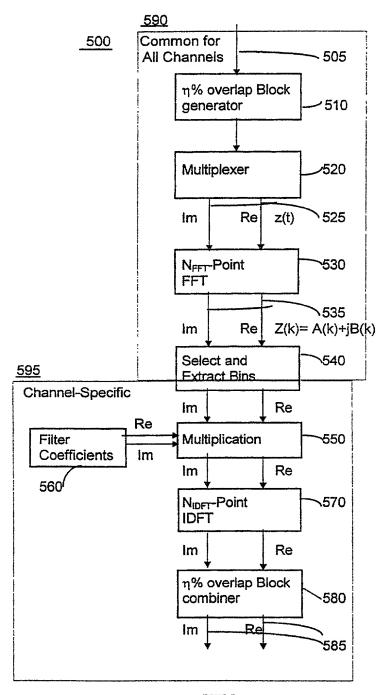


Fig. 5

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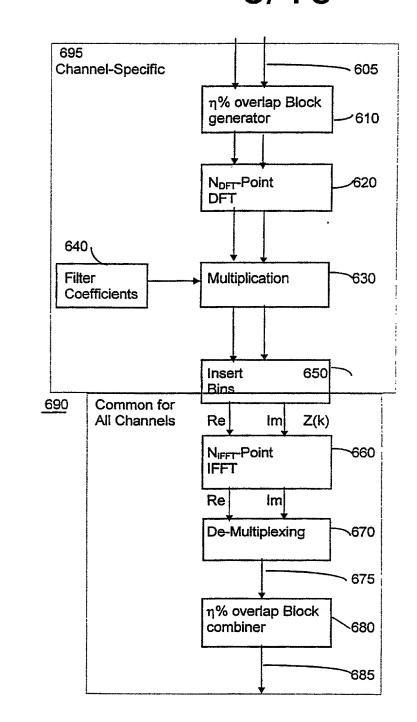


Fig. 6

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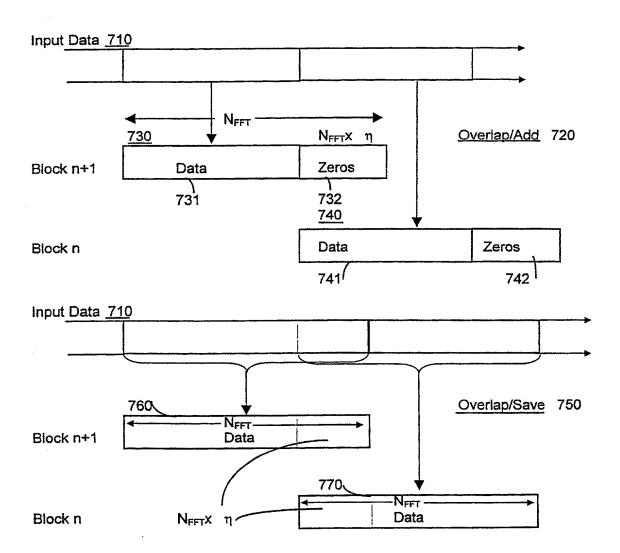
AND DE-CHANNELISATION

INVENTOR(S):

RICHARD HELLBERG

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 $\eta\%$ Overlap Block Generator

Fig. 7

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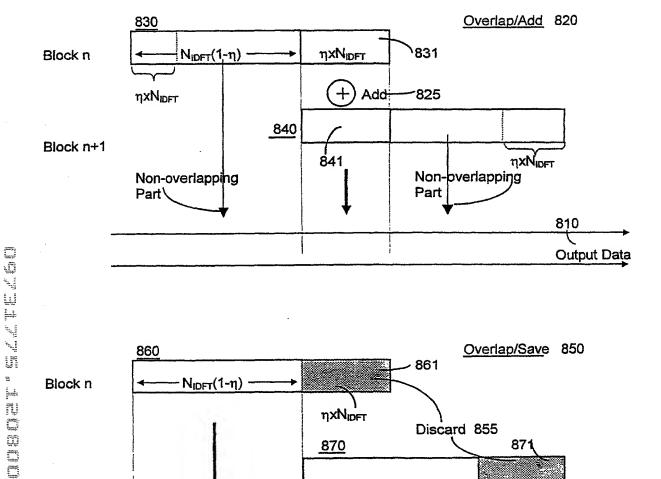
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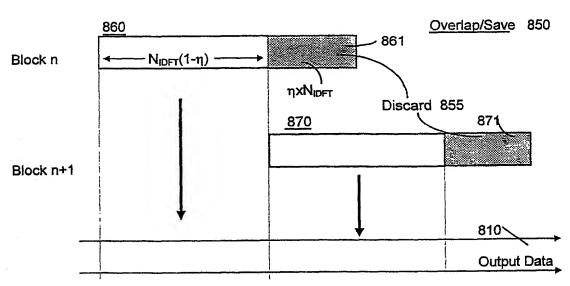
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η% Overlap Block Combiner

Fig. 8

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AND DE-CHANNELISATION

inventor(s):

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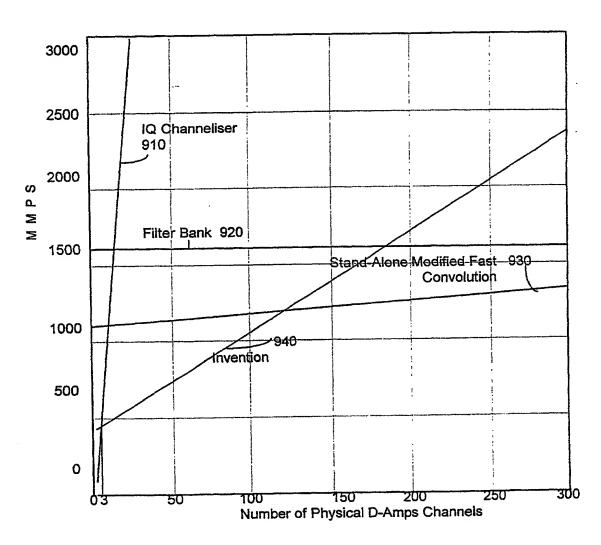


Fig. 9

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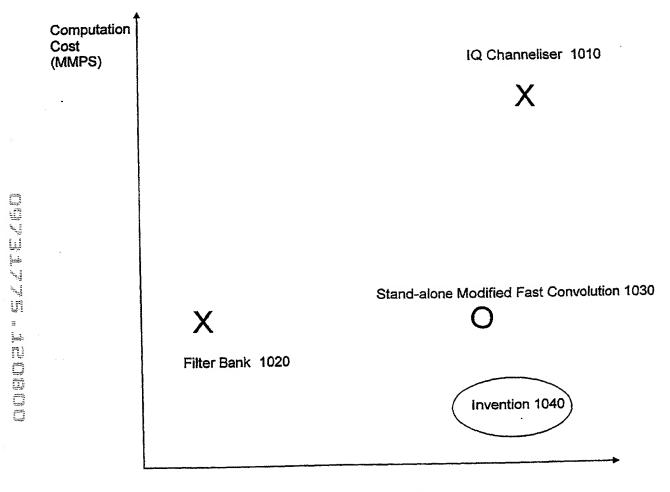
INVENTOR(s):

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Flexibility

For Between 10-100 Channels

Fig. 10